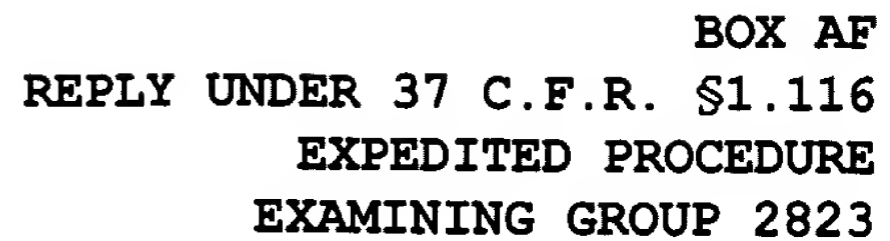


AF/2823



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| | | | |
|-------------|---|-----------|-----------|
| Applicant: | Jun S. LEE | Conf.: | 5244 |
| Serial No.: | 09/722,583 | Art Unit: | 2823 |
| Filed: | November 28, 2000 | Examiner: | B. Kebede |
| For: | METHOD FOR FABRICATING CAPACITOR OF SEMICONDUCTOR DEVICE | | |

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. §1.8.

☐ The enclosed document is being transmitted via facsimile.

| | CLAIMS REMAINING AFTER AMENDMENT | | | | | HIGHEST NUMBER PREVIOUSLY PAID FOR | PRESENT EXTRA | RATE | ADDITIONAL FEE |
|---|---|---|----|---|---|---|------------------|--------|-------------------|
| TOTAL | 8 | - | 20 | = | 0 | | \$ 18 | \$0.00 | |
| INDEPENDENT | 2 | - | 3 | = | 0 | | \$ 84 | \$0.00 | |
| <input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM | | | | | | | \$280 | \$0.00 | |
| | | | | | | | TOTAL | \$0.00 | |

Serial No. 09/722,583

Docket No. 465-758P

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§1.17 and 1.136(a). \$0.00 for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of \$0.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17, particularly extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By Esther H. Chong #40,953
for Joseph A. Kolasch, #22,463

465-758P

Attachments

JAK:SB:rk

SB

P.O. Box 747

Falls Church, VA 22040-0747

(703) 205-8000



BOX AF
REPLY UNDER 37 C.F.R. §1.116
EXPEDITED PROCEDURE REQUESTED

PATENT
465-758P

#10
Crawford
J. M. Miller
9/24/02

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Jun S. LEE Conf.: 5244
Serial No.: 09/722,583 Art Unit: 2823
Filed: November 28, 2000 Examiner: B. Kebede
For: METHOD FOR FABRICATING CAPACITOR OF
SEMICONDUCTOR DEVICE

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TECHNOLOGY CENTER 2800

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents
Washington, DC 20231

September 19, 2002

Sir:

Attached are corrected formal drawings for FIGS. 1A-1E and 2A-2E in connection with the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies to charge payment or to credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17, particularly extension of time fees.

Respectfully submitted,
BIRCH, STEWART, KOLASCH & BIRCH, LLP

By: Ester H. Chong #40,953
for Joseph A. Kolasch
Reg. No. 22,463

465-758P
Attachments
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